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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/077,847	02/20/2002	Kuan-Yu Lee	SUND 279	3153	
7590 01/04/2005		EXAMINER			
RABIN & BERDO, P.C.			TRAN, VINCENT HUY		
Suite 500 1101 14th Stree	t. N.W.		ART UNIT	PAPER NUMBER	
Washington, DC 20005			2115		
			DATE MAILED: 01/04/2005	DATE MAILED: 01/04/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application No.	Applicant(s)			
		10/077,847	LEE ET AL.			
		Examiner	Art Unit	_		
	: 	Vincent T. Tran	2115			
Period fo	The MAILING DATE of this communication apport	pears on the cover sheet with the c	orrespondence address			
THE - Exte after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period one to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tin y within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status			· :			
1)	Responsive to communication(s) filed on	_·				
2a) <u></u> □	2a) ☐ This action is FINAL . 2b) ☑ This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.			
Disposit	ion of Claims		:			
4) 🖂	Claim(s) 1-10 is/are pending in the application					
•	4a) Of the above claim(s) is/are withdrawn from consideration.					
5)[Claim(s) is/are allowed.		:			
6)⊠	Claim(s) <u>1-10</u> is/are rejected.		: :			
7)	Claim(s) is/are objected to.					
. 8)	Claim(s) are subject to restriction and/o	r election requirement.				
Applicat	ion Papers					
9) 🏹	The specification is objected to by the Examine	.	:			
	The drawing(s) filed on is/are: a)⊠ acc		Examiner.			
,	Applicant may not request that any objection to the					
	Replacement drawing sheet(s) including the correct	•	• • • • • • • • • • • • • • • • • • • •			
11)	The oath or declaration is objected to by the Ex	· · · · · · · · · · · · · · · · · · ·				
Priority ι	under 35 U.S.C. § 119		:			
•	Acknowledgment is made of a claim for foreign All b) Some * c) None of: Certified copies of the priority document)-(d) or (f).			
	 Certified copies of the priority document Certified copies of the priority document 		on No			
	3. Copies of the certified copies of the prior		•			
,	application from the International Bureau		ed in this National Stage			
* 5	See the attached detailed Office action for a list	• • • • • • • • • • • • • • • • • • • •	ed			
	:		<u>→</u> :			
	:		:			
Attachmen	t(s)					
1) Notic	ce of References Cited (PTO-892)	4) Interview Summary	(PTO-413)			
	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da				
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) or No(s)/Mail Date	6) Other:	ratent Application (PTO-152)	_		

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DETAILED ACTION

Specification

1. Claim 8 objected to because of the following informalities: "TC74/HC374" should be "TC74HC374". Appropriate correction is required.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

3. Claim 8 rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. "TC74/HC374".

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) The invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

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The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

- 5. Claims 1, 4, 5, 6, 9, 10 are rejected under 35 U.S.C. 102(e) as being anticipated by So et al (U.S. Patent No. 6,307,776).
- 6. As per claims 1 and 6,

So et al teach the invention comprising:

a memory having a memory pin [pin connected to data correction 615, fig. 6] and has a refresh operation [col. 9, line 34 et seq.];

a control processing unit¹ having a data pin [output pin of mux 665, fig. 6]; and a buffer [610] for receiving an input signal [Din] and feeding the input signal into the control processing unit according to a control signal synchronized with refresh operation [information stored in buffer 610 is outputted during a refresh operation, col. 10, lines 19 – 21].

7. As per claims 4 and 9, the memory is DRAM [col. 4, line 42-46].

¹ The control-processing unit is not explicitly shown in the reference. It is connected to the output of the memory as shown in fig. 6.

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As per claims 5 and 10, the control signal is inherently generated by the control processing unit.

Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claims 2, 3, 7, 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over So et al (U.S. Patent No. 6,307,776).
- 8. As per claims 2 and 7, So et al do not detail the control processing unit coupled to the memory. It would have been obvious to one of ordinary skill in the art the generic unit which is coupled to the memory encompasses different processing unit including the claimed ASIC because the special structure of the processing unit does not affect the data output switching operation of the memory and buffer.
- 9. As per claims 3 and 8, So et al do not teach the claimed buffer model. It would have been obvious to one of ordinary skill in the art the generic buffer encompasses different buffers including the claimed buffer type because the special structure of the buffer does not affect the data output switching operation of the memory and buffer.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent T. Tran whose telephone number is (571) 272-7210. The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas c. Lee can be reached on (571) 272-3667. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free):

Vincent Tran